



# TXTJ290P02U-G

## Silicon P-Channel Power MOSFET

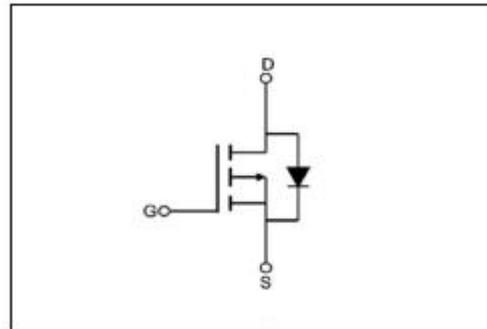
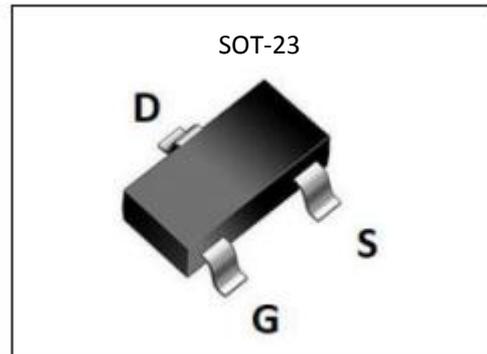
### General Description :

The TXTJ290P02U-G uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications. The package form is SOT-23, which accords with the RoHS standard and Halogen-free.

$V_{DSS}$	-20	V
$I_D$	-6	A
$P_D$	1	W
$R_{DS(ON)type}$	29	m $\Omega$

### Features :

- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation



### Applications :

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

### Absolute ( $T_c = 25^\circ\text{C}$ unless otherwise specified ) :

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	-20	V
$I_D$	Continuous Drain Current	-6	A
$I_{DM}$	Pulsed Drain Current	-22	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$P_D$	Power Dissipation	1	W
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	-55 to 150	$^\circ\text{C}$



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**Electrical Characteristics** (  $T_c = 25^\circ\text{C}$  unless otherwise specified ) :

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Unit s
			Min.	Typ.	Max.	
$V_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20	--	--	V
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS}=-20V, V_{GS}=0V, T_a=25^\circ\text{C}$	--	--	1.0	$\mu A$
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+12V$	--	--	0.1	$\mu A$
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-12V$	--	--	-0.1	$\mu A$

ON Characteristics <sup>a3</sup>						
Symbol	Parameter	Test Conditions	Rating			Unit s
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=-4.5V, I_D=-2A$	--	29	35	m $\Omega$
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=-2.5V, I_D=-1A$	--	34	53	m $\Omega$
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	0.4	0.6	1.0	V

Pulse width  $t_p \leq 380\mu s, \delta \leq 2\%$

Dynamic Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Unit s
			Min.	Typ.	Max.	
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=-10V$	--	765	--	pF
$C_{oss}$	Output Capacitance	$f=1.0\text{MHz}$	--	121	--	
$C_{rss}$	Reverse Transfer Capacitance		--	73	--	

Resistive Switching Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=-10V, I_D=-2A$ $V_{GS}=-4.5V, R_G=3\Omega$	--	11	--	ns
$t_r$	Rise Time		--	30	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	50	--	
$t_f$	Fall Time		--	50	--	
$Q_g$	Total Gate Charge	$V_{DD}=-10V, I_D=-2A$	--	9	--	nC
$Q_{gs}$	Gate to Source Charge	$V_{GS}=-4.5V$	--	1.7	--	
$Q_{gd}$	Gate to Drain Charge		--	2.2	--	



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## *Silicon P-Channel Power MOSFET*

### Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I <sub>S</sub>	Continuous Source Current <sup>a2</sup> (Body Diode)		--	--	-6	A
V <sub>SD</sub>	Diode Forward Voltage <sup>a3</sup>	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V	--	--	-1.2	V

Symbol	Parameter	Typ.	Units
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient. <sup>a2</sup>	110	°C /W

<sup>a2</sup> : Surface Mounted on FR4 Board, t ≤ 10sec.

<sup>a3</sup> : Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.

<sup>a4</sup> : Guaranteed by design, not subject to production

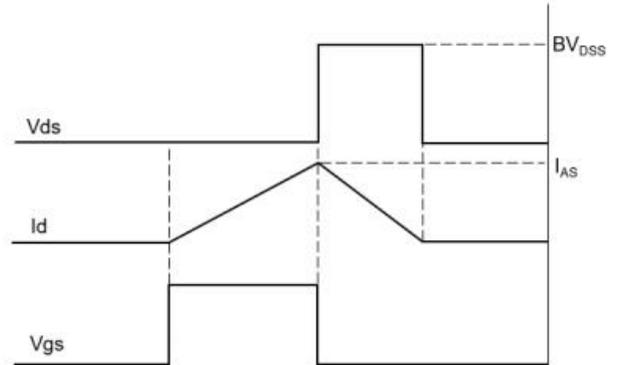
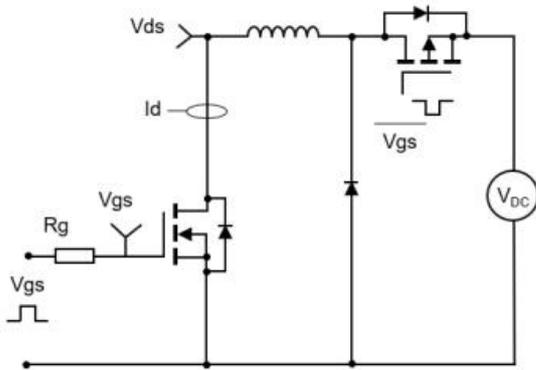


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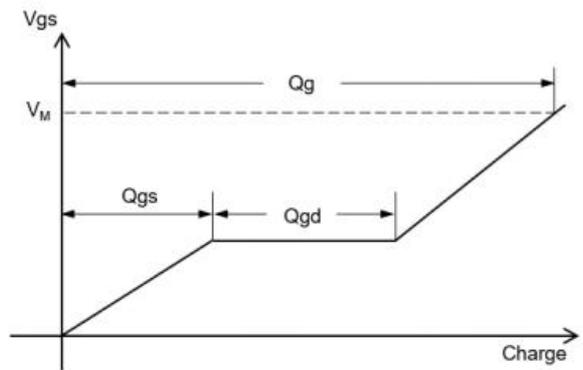
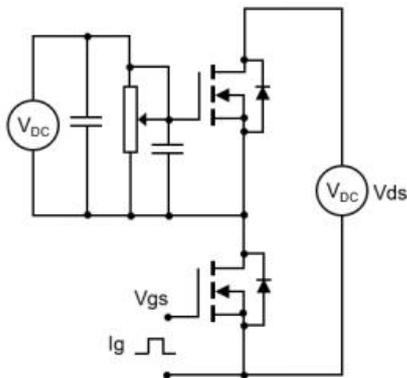
## Silicon P-Channel Power MOSFET

### Test Circuit & Waveform

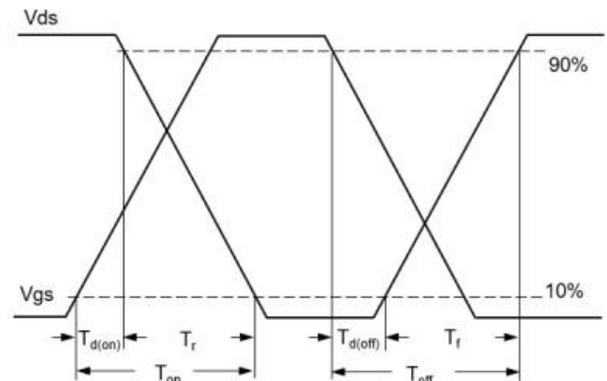
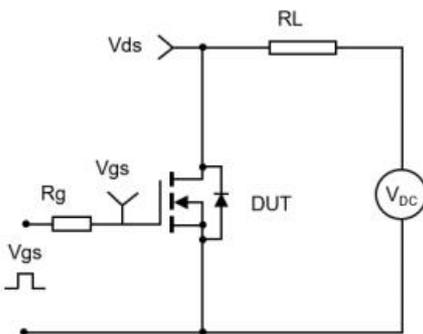
#### 1. Unclamped Inductive Switching Test Circuit & Waveform



#### 2. Gate Charge Test Circuit & Waveform



#### 3. Resistive Switching Test Circuit & Waveform



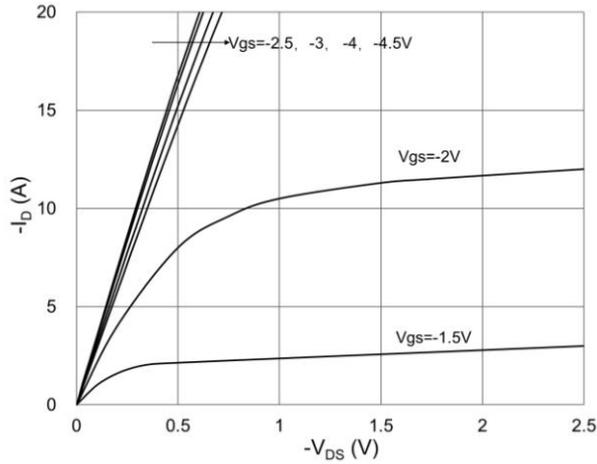


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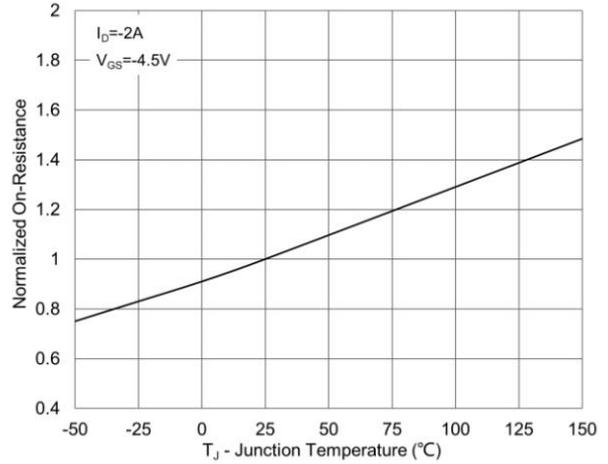
## Silicon P-Channel Power MOSFET

### Characteristics Curve :

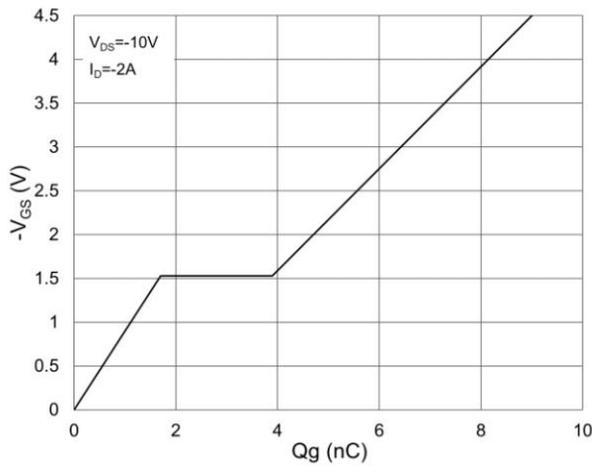
**Fig1 Output Characteristics**



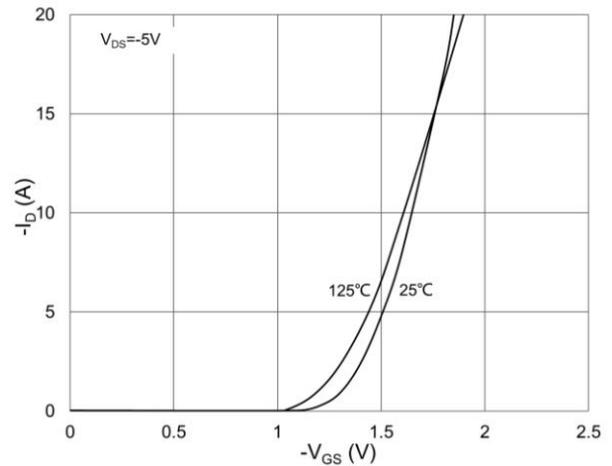
**Fig2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



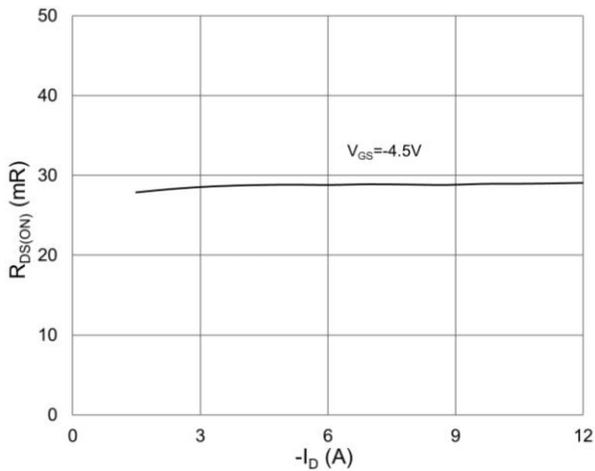
**Fig3 Gate Charge Waveform**



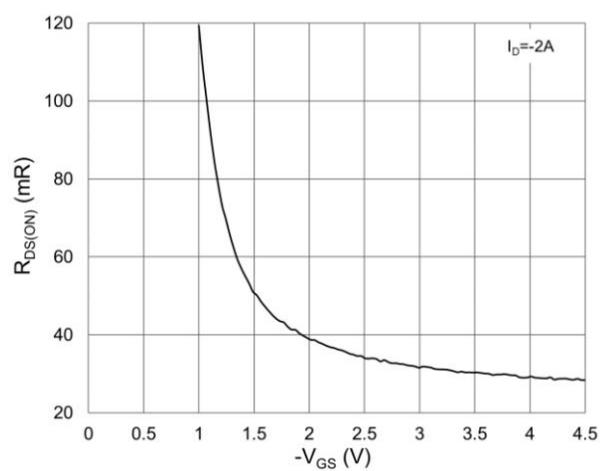
**Fig4 Transfer Characteristics**



**Fig5  $R_{DS(on)}$  vs. Drain Current and Gate Voltage**



**Fig6  $R_{DS(on)}$  vs. Gate Voltage**

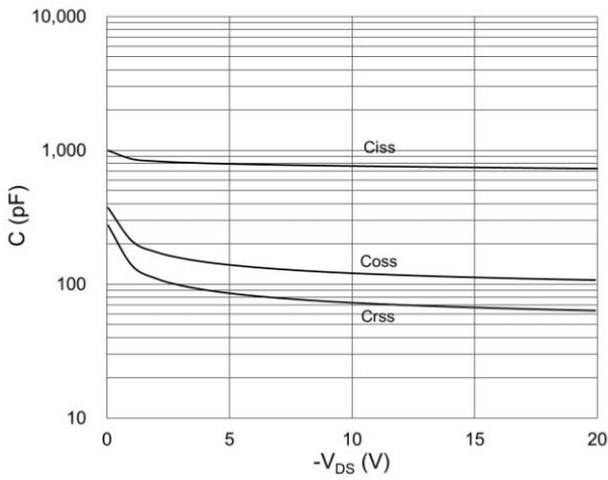




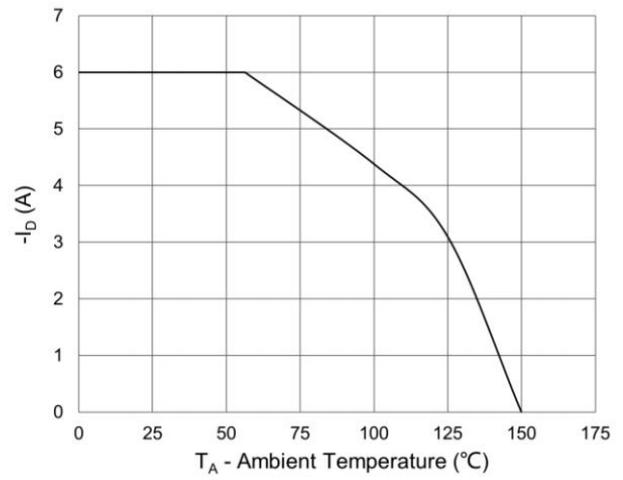
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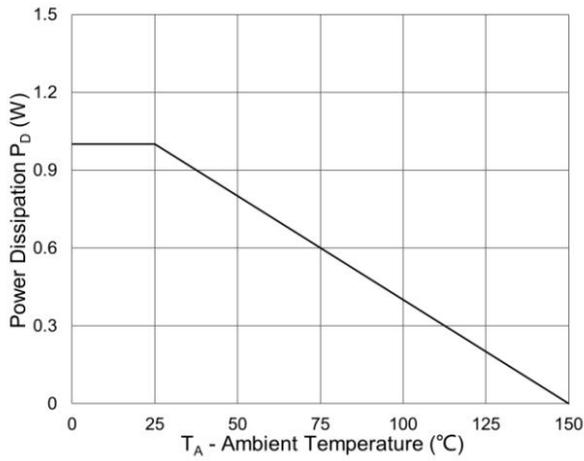
**Fig7 Capacitance Characteristics**



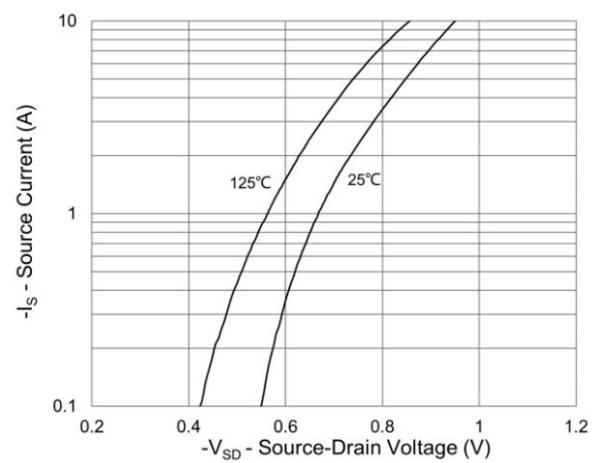
**Fig8 Drain Current Derating**



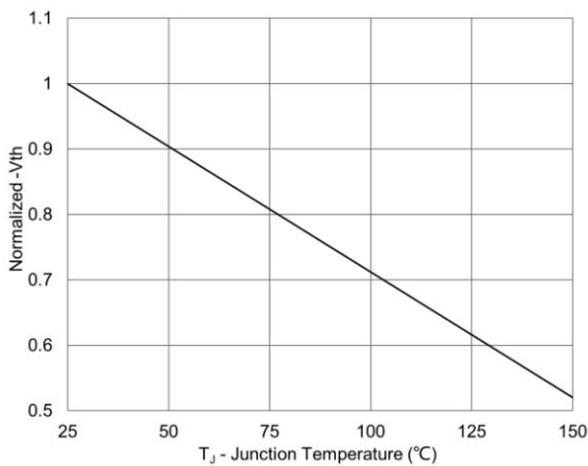
**Fig9 Power Dissipation**



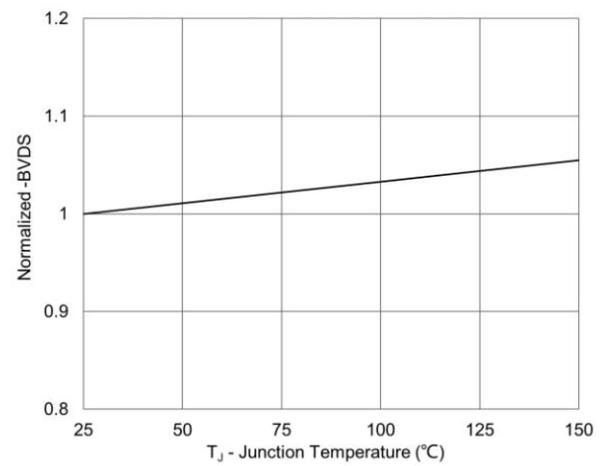
**Fig10 Source-Drain Diode Forward Characteristics**



**Fig11 Normalized Threshold Voltage vs.  $T_J$**



**Fig12 Normalized Breakdown Voltage vs.  $T_J$**





# TXTJ290P02U-G

## Silicon P-Channel Power MOSFET

Fig13 Maximum Safe Operation Area

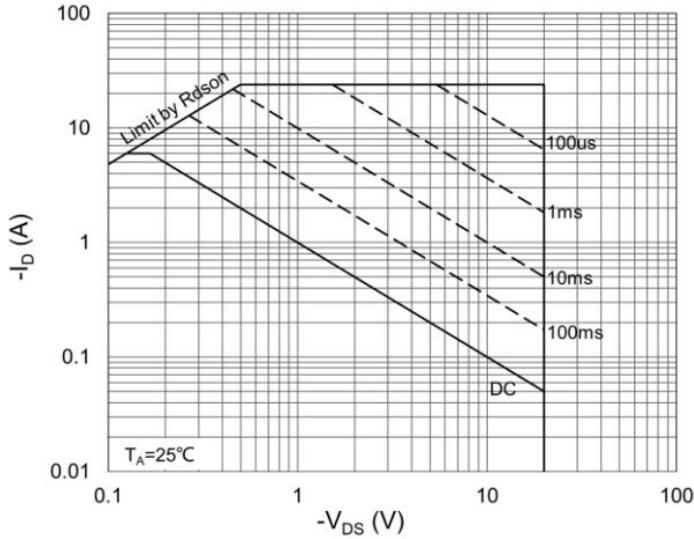
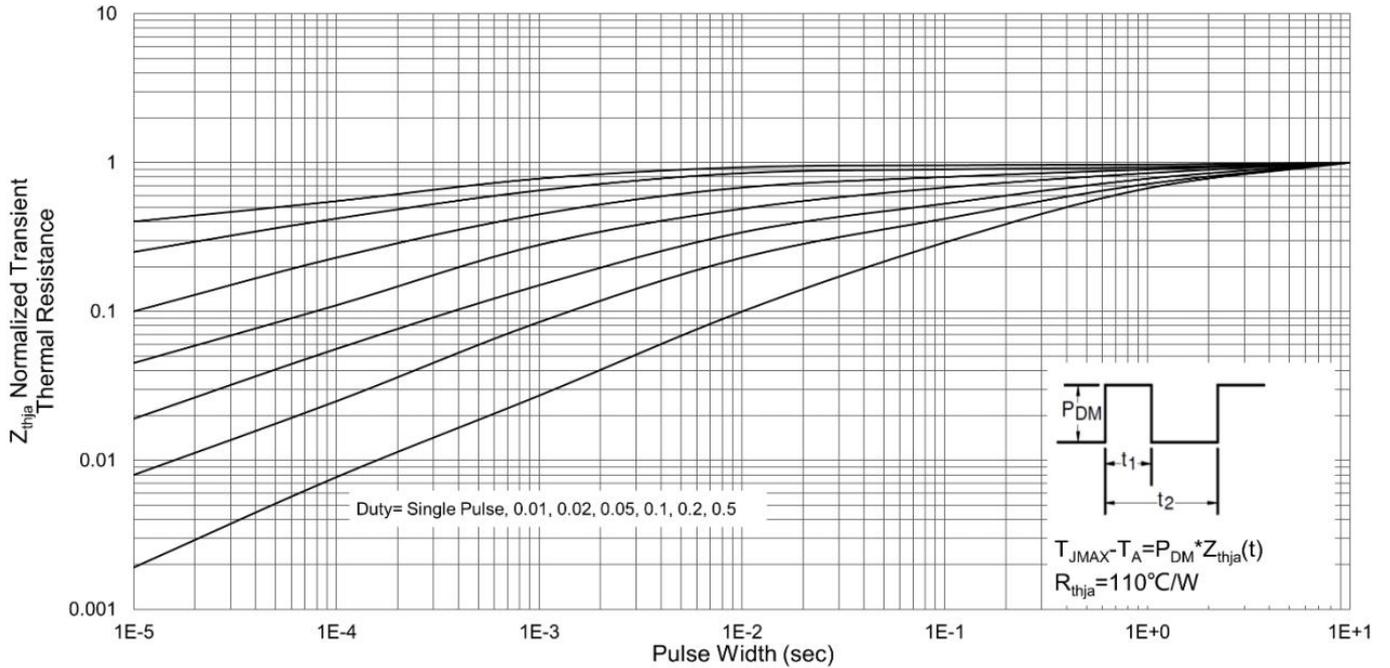


Fig14 Normalized Transient Impedance

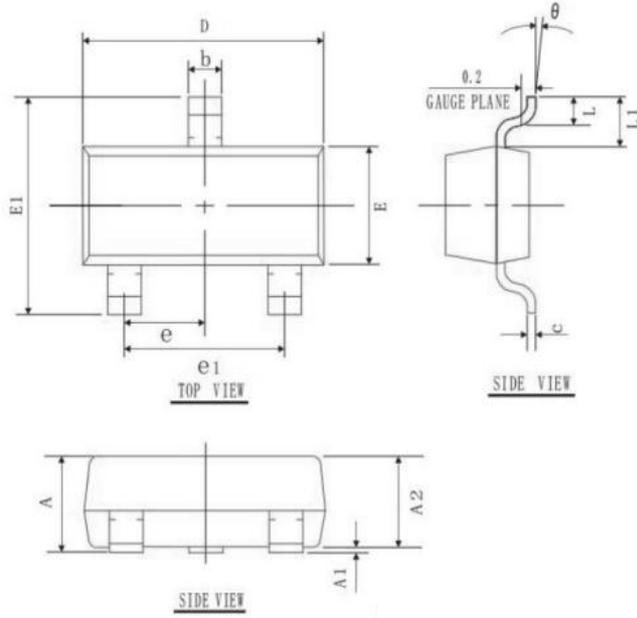




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## SOT-23 Package Information



COMMON DIMENSIONS  
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.90	1.05	1.20
A1	0.00	0.05	0.10
A2	0.90	1.00	1.10
b	0.30	0.40	0.50
c	0.08	0.10	0.15
D	2.80	2.90	3.00
E	1.20	1.30	1.40
E1	2.30	2.40	2.50
L	0.30	0.40	0.50
$\theta$	0°	5°	10°
L1	0.55 REF		
e	0.95 BSC		
e1	1.90 REF		